

FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 01827.0037.US00	SERIAL NO. 09/602,690
	APPLICANT: Donald B. Eidson, et al.	
	FILING DATE: June 23, 2000	GROUP: 2734 2133

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE	
			None				

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO	
			None				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
<i>[Signature]</i>		Benedetto, S. et al., <i>A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes</i> , TDA Progress Report 42-127 , November 15, 1996, pages 1-20.
<i>[Signature]</i>		Benedetto, S. et al, <i>Serial Concatenated Trellis Coded Modulation with Iterative Decoding: Design and Performance</i> , Dipartimento di Electronica, Politecnico di Torino, and Jet Propulsion Laboratory, California Institute of Technology , 1997 IEEE, pages 38-43.
<i>[Signature]</i>		Benedetto, Sergio, <i>Turbo Codes: Analysis, Design, Iterative Decoding and Applications</i> , International Courses for Telecom and Semiconductor Professionals , Barcelona, Spain, October 25-29, 1999, 4 pages.
<i>[Signature]</i>		Berrou, Claude et al., <i>Near Shannon Limit Error - Correcting Coding and Decoding: Turbo-Codes (I)</i> , Integrated Circuits for Telecommunication Laboratory et al., 1993 IEEE, pages 1064 - 1070.
<i>[Signature]</i>		Divsalar, D. et al., <i>Turbo Trellis Coded Modulation with Iterative Decoding for Mobile Satellite Communications</i> , Jet Propulsion Laboratory, California Institute of Technology , 4800 Oak Grove, Dr., Pasadena, CA 91109, (7 pages).
<i>[Signature]</i>		Hoeher, Peter et al., <i>"Turbo DPSK": Iterative Differential PSK Demodulation and Channel Decoding</i> , IEEE Transactions on Communications , Vol. 47, No. 6, June, 1999, (15 pages).
<i>[Signature]</i>		Narayanan, Krishna R., et al., <i>A Serial Concatenation Approach to Iterative Demodulation and Decoding</i> , IEEE Transactions on Communications , Vol., 47, No. 7, July, 1999, pages 956 -

EXAMINER: <i>[Signature]</i> <i>Dmg to Nguyen</i>	DATE CONSIDERED: <i>11/05/03</i> <i>5/20/2003</i>
EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant	

**LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

ATTY.CKET NO.

01827.0037.US00

SERIAL NO:

09/602,690

APPLICANT:

Donald B. Eidson, et al.

FILING DATE:

June 23, 2000

GROUP:

2734-2133

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
AA		6,023,783	02/08/00	Divsalar et al.	714	792	05/15/97

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
				None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AB		Divsalar et al., <i>Serial and Hybrid Concatenated Codes with Applications</i> ; Jet Propulsion Laboratory, California Institute of Technology, Pasadena, CA; International Symposium on Turbo Codes, Brest, France, 1997. Pages 80-87
----	--	---

RECEIVED
JAN - 8 2001
TC 2600 MAILROOM

EXAMINER:

Doan X. Nguyen

DATE CONSIDERED:

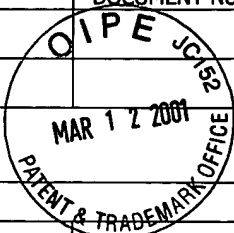
11/05/03
J/20/2003

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant

FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 01827.0037.00US00	SERIAL NO. 09/602,690
	APPLICANT: Donald B. Eidson, et al.	
	FILING DATE: June 23, 2000	GROUP: 2734 2133

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
			None			



FOREIGN PATENT DOCUMENTS

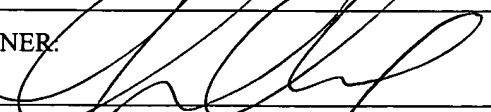
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES	TRANSLATION NO
			None				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AA	Benedetto, S. and Montorsi, G., <i>Iterative decoding of serially concatenated convolutional codes</i> ; Electronics Letters , Vol. 32, No. 13, June 1996, pages 1186-1188.
AB	Benedetto, S. and Montorsi, G., <i>Serial concatenation of block and convolutional codes</i> ; Electronics Letters , Vol. 32, No. 10, May 1996, pages 887-888.
AC	Robertson, P., et al., <i>A Comparison of Optimal and Sub-Optimal MAP Decoding Algorithms Operating in the Log Domain</i> ; IEEE , 1995, pages 1009-1013.
AD	Viterbi, Andrew J., <i>An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes</i> ; IEEE Journal on Selected Areas in Communications , Vol. 16, No. 2, February 1998, pages 260-264.

RECEIVED
MAR 14 2001
Technology Center 8300

RECEIVED
MAR 14 2001
Technology Center 2600

EXAMINER: 	DATE CONSIDERED: 11/05/02
EXAMINER. Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant	

**STATEMENT OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT**

JUN 18 2001 (Use several sheets if necessary)

APPLICANT:
Donald B. Eidson, et al.

FILING DATE:
June 23, 2000

GROUP:
~~2734~~ 2133

U.S. PATENT DOCUMENTS

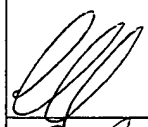
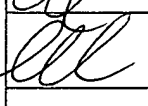
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
			None			

RECEIVED
JUN 21 2001
Technology Center 2600

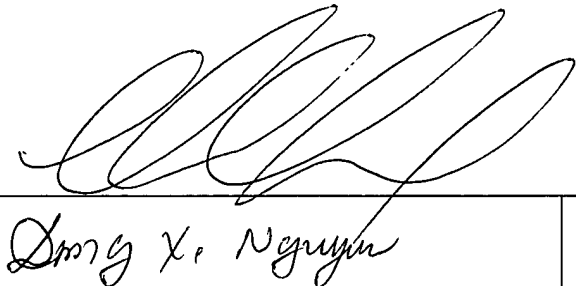
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
			None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	AA	Benedetto, S. and Divsalar, D., <i>Serial Concatenated Trellis Coded Modulation with Rate-1 Inner Code</i> ; International Courses for Telecom and Semiconductor Professionals, Course 909 Turbo Codes: Analysis, Design, Iterative Decoding and Applications , October 1999, pages 324-339.
	AB	Divsalar, D., et al., <i>Serial Turbo Trellis Coded Modulation with Rate-1 Inner Code</i> ; IEEE , June 2000, page 194.

EXAMINER:



DATE CONSIDERED:

11/03/03
5/20/2003

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant

FORM PTO-1449

ATTY. Docket NO.
01827.0037.00US00SERIAL NO.
09/602,690LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENTAPPLICANT:
Donald B. Eidson, et al.FILING DATE:
June 23, 2000GROUP:
2734 2133

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
			None			

COPY OF PAPERS
ORIGINALLY FILED

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
			None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AA	Peleg, M. et al., <i>On Interleaved, Differentially Encoded Convolutional Codes</i> ; IEEE Transactions on information Theory , November 1999, pages 2572-2582.
AB	Divsalar, D., et al., <i>Serial Concatenated Trellis Coded Modulation with Rate-1 Inner Code</i> ; 2000 IEEE Global Telecommunications Conference , November 2000, pages 777-782

RECEIVED

MAR 06 2002

Technology Center 2400

COPY OF PAPERS
ORIGINALLY FILED

EXAMINER:

Dong X. Nguyen

DATE CONSIDERED:

11/05/03
5/20/2003

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant

01827.0037.00US00

09/602,690

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S
INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT:

Donald B. Eidson, et al.

FILING DATE:

June 23, 2000

GROUP:

2734

U.S. PATENT DOCUMENTS

INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
			None			

RECEIVED


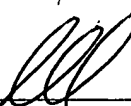

FEB 05 2004

Technology Center 2100

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES	TRANSLATION NO
			None				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	A1	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals, Jet Propulsion Laboratory, October 25-29, 1999.
	A2	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART I, Jet Propulsion Laboratory, October 25-29, 1999.
	A3	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART II, Jet Propulsion Laboratory, October 25-29, 1999.

EXAMINER:

DATE CONSIDERED:

2/9/04

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant